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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michel Koopmans

Serial No.: Not yet assigned

Filed: September 21, 2001

For: BUMPING TECHNOLOGY IN
STACKED DIE CONFIGURATIONS

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 4882US (01-0229)



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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

DOCUMENTS

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
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Other Documents

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Garrou, Philip, "Wafer-Level Packaging Has Arrived", pp. 1-10, Semiconductor International, <http://www.semiconductor.net/semiconductor/issues/2000/200010/six001003waf.asp>, July 16, 2001.

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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KWP/hlg:dlm
Enclosures: Form PTO-1449
Copy of documents cited

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